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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor application of: Sailesh Chittipeddi

Serial No.: 09/596,382

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For: A PROCESS FOR MANUFACTURING AN INTEGRATED
CIRCUIT INCLUDING A DUAL-DAMASCENE STRUCTURE
AND A CAPACITOR

Group No.: 2812

Examiner: Tsai, H. Jey

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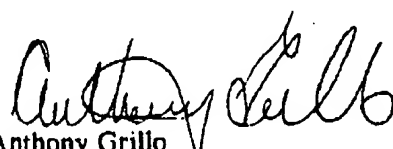
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